

## STATEMENT OF THE CLAIMS

1. (currently amended) Method for providing transmit flow control for multiple signal streams over a single ~~ETHERNET~~ Ethernet link, comprising:

receiving PDUs (protocol data units) from multiple streams at a first MAC (media access control) client;

encapsulating each PDU in a MAC frame which includes an identification of the stream to which the PDU belongs;

transmitting the MAC frames over an ~~ETHERNET~~ Ethernet link to a second MAC client;

receiving the MAC frames at the second MAC client;

decapsulating each PDU;

forwarding each PDU to a port buffer associated with the stream identified in the MAC frame from which each PDU was decapsulated;

monitoring each buffer for fullness; and

transmitting a ~~PAUSE~~ Pause control frame from the second MAC client to the first MAC client, the ~~PAUSE~~ Pause control frame indicating the fullness condition of each buffer.

2. (currently amended) The method according to claim 1, further comprising:

controlling the flow of signal streams by temporarily halting the transmission of PDUs belonging to streams associated with buffers which are indicated as congested by the ~~PAUSE~~ Pause control frame.

3. (original) The method according to claim 1, wherein:

each MAC frame includes a pre-pended address field which identifies the stream with which the encapsulated PDU is associated.

4. (original) The method according to claim 1, wherein:

the identification is mapped onto an IEEE 802.1Q VLAN (virtual local area network) tag within the MAC frame.

5. (original) The method according to claim 1, wherein:

the identification is an IEEE 802.1Q VLAN (virtual local area network) tag within the MAC frame which is mapped to a port.

6. (currently amended) The method according to claim 1, wherein:

the ~~PAUSE~~ Pause control frame includes a single bit identifier for each buffer for indicating the fullness condition of the associated buffer.

7. (currently amended) The method according to claim 6, wherein:

each single bit identifies an ~~XON/XOFF~~ Xon/Xoff condition.

8. (currently amended) The method according to claim 1, wherein:

the ~~PAUSE~~ Pause control frame includes a two bit identifier for each buffer for indicating the fullness condition of the associated buffer.

9. (currently amended) The method according to claim 8, wherein:

each two bit identifier identifies an ~~XON/XOFF/NOCHANGE~~  
Xon/Xoff/NoChange condition.

10. (currently amended) The method according to claim 1, wherein:

the ~~PAUSE~~ Pause control frame includes a ~~PAUSE~~ Pause timer value.

11. (currently amended) The method according to claim 10, wherein:

the ~~PAUSE~~ Pause timer value is set to zero when the ~~PAUSE~~ Pause control frame indicates that no buffer is experiencing congestion.

12. (currently amended) The method according to claim 11, wherein:

the ~~PAUSE~~ Pause timer value is set to a pre-programmed Pause Time Value when the ~~PAUSE~~ Pause control frame indicates that at least one buffer is experiencing congestion.

13. (currently amended) The method according to claim 12, further comprising:

setting a pause refresh timer each time a ~~PAUSE~~ Pause control frame is transmitted; and

transmitting a ~~PAUSE~~ Pause control frame at the expiration of the pause refresh timer if no ~~PAUSE~~ Pause control frame was transmitted since the pause refresh timer was set.

14. (currently amended) The method according to claim 13, further comprising:

setting a pause delay timer each time a ~~PAUSE~~ Pause control frame is transmitted; and

transmitting a ~~PAUSE~~ Pause control frame at the expiration of the pause delay timer if congestion conditions have changed since the last ~~PAUSE~~ Pause control frame was transmitted.

15. (original) The method according to claim 14, wherein:

the pause delay timer is of shorter duration than the pause refresh timer.

16. (currently amended) A method for providing flow control for multiple signal streams over a single ~~ETHERNET~~ Ethernet link, comprising:

receiving MAC frames from a MAC client, each frame containing a PDU and an indication of the stream to which the PDU belongs;

decapsulating the PDUs and storing each PDU in a buffer associated with the stream indicated in the MAC frame;

monitoring the fullness of each buffer; and

transmitting a ~~PAUSE~~ Pause control frame to the MAC client, the ~~PAUSE~~ Pause control frame indicating the fullness condition of each buffer.

17. (currently amended) The method of claim 16, wherein:

the ~~PAUSE~~ Pause control frame includes a single bit identifier for each buffer for indicating the fullness condition of the associated buffer.

18. (currently amended) The method according to claim 17, wherein:

each single bit identifies an ~~XON/XOFF~~ Xon/Xoff condition.

19. (currently amended) The method according to claim 16, wherein:

the ~~PAUSE~~ Pause control frame includes a two bit identifier for each buffer for indicating the fullness condition of the associated buffer.

20. (currently amended) The method according to claim 19, wherein:

each two bit identifier identifies an ~~XON/XOFF/NOCHANGE~~  
Xon/Xoff/NoChange condition.

21. (currently amended) The method according to claim 16, wherein:

the ~~PAUSE~~ Pause control frame includes a ~~PAUSE~~ Pause timer value.

22. (currently amended) The method according to claim 21, wherein:

the ~~PAUSE~~ Pause timer value is set to zero when the ~~PAUSE~~ Pause control frame indicates that no buffer is experiencing congestion.

23. (currently amended) The method according to claim 22, wherein:

the ~~PAUSE~~ Pause timer value is set to a pre-programmed Pause Time Value when the ~~PAUSE~~ Pause control frame indicates that at least one buffer is experiencing congestion.

24. (currently amended) The method according to claim 23, further comprising:

setting a pause refresh timer each time a ~~PAUSE~~ Pause control frame is transmitted; and

transmitting a ~~PAUSE~~ Pause control frame at the expiration of the pause refresh timer if no ~~PAUSE~~ Pause control frame was transmitted since the pause refresh timer was set.

25. (currently amended) The method according to claim 24, further comprising:

setting a pause delay timer each time a ~~PAUSE~~ Pause control frame is transmitted; and

transmitting a ~~PAUSE~~ Pause control frame at the expiration of the pause delay timer if congestion conditions have changed since the last ~~PAUSE~~ Pause control frame was transmitted.

26. (original) The method according to claim 25, wherein:

the pause delay timer is of shorter duration than the pause refresh timer.

27. (currently amended) ~~An apparatus~~ A system for providing flow control for multiple signal streams over a single ~~ETHERNET~~ Ethernet link, comprising:

a first MAC (media access control) client; and

a second MAC client coupled to said first MAC client by the ~~ETHERNET~~ Ethernet link,

said first MAC client having

means for receiving PDUs (protocol data units) from multiple streams,

means for encapsulating each PDU in a MAC frame which includes an identification of the stream to which the PDU belongs,

means for transmitting the MAC frames over the ~~ETHERNET~~ Ethernet link to said second MAC client,

said second MAC client having

means for receiving the MAC frames transmitted by said first MAC client,

means for decapsulating each PDU,

means for forwarding each PDU to a port buffer associated with the stream identified in the MAC frame from which each PDU was decapsulated,

means for monitoring each buffer for fullness, and

means for transmitting a ~~PAUSE~~ Pause control frame to said first MAC client, the ~~PAUSE~~ Pause control frame indicating the fullness condition of each buffer.



28. (currently amended) The ~~apparatus~~ system according to claim 27, further comprising:

means for controlling the flow of said multiple signal streams in response to said ~~PAUSE~~ Pause control frame, including means for temporarily halting the transmission of PDUs belonging to streams associated with buffers indicated as congested by said ~~PAUSE~~ Pause control frame.

29. (currently amended) The ~~apparatus~~ system according to claim 27, wherein:

each MAC frame includes a pre-pended address field which identifies the stream with which the encapsulated PDU is associated.

30. (currently amended) The ~~apparatus~~ system according to claim 27, wherein:

the identification is mapped onto an IEEE 802.1Q VLAN (virtual local area network) tag within the MAC frame.

31. (currently amended) The ~~apparatus~~ system according to claim 27, wherein:

the identification is an IEEE 802.1Q VLAN (virtual local area network) tag within the MAC frame which is mapped to a port.

32. (currently amended) The ~~apparatus~~ system according to claim 27, wherein:

the ~~PAUSE~~ Pause control frame includes a single bit identifier for each buffer for indicating the fullness condition of the associated buffer.

33. (currently amended) The ~~apparatus~~ system according to claim 32, wherein:

each single bit identifies an ~~XON/XOFF~~ Xon/Xoff condition.

34. (currently amended) The ~~apparatus~~ system according to claim 31, wherein:

the ~~PAUSE~~ Pause control frame includes a two bit identifier for each buffer for indicating the fullness condition of the associated buffer.

35. (currently amended) The ~~apparatus~~ system according to claim 34, wherein:

each two bit identifier identifies an ~~XON/XOFF/NOCHANGE~~  
Xon/Xoff/NoChange condition.

36. (currently amended) The ~~apparatus~~ system according to claim 31, wherein:

the ~~PAUSE~~ Pause control frame includes a ~~PAUSE~~ Pause timer value.

37. (currently amended) The ~~apparatus~~ system according to claim 36, wherein:

the ~~PAUSE~~ Pause timer value is set to zero when the ~~PAUSE~~ Pause control frame indicates that no buffer is experiencing congestion.

38. (currently amended) The ~~apparatus~~ system according to claim 37, wherein:

the ~~PAUSE~~ Pause timer value is set to a pre-programmed Pause Time Value when the ~~PAUSE~~ Pause control frame indicates that at least one buffer is experiencing congestion.

39. (currently amended) The ~~apparatus~~ system according to claim 38, wherein:

said second MAC client includes means for setting a pause refresh timer each time a ~~PAUSE~~ Pause control frame is transmitted,

a ~~PAUSE~~ Pause control frame being transmitted at the expiration of the pause refresh timer if no ~~PAUSE~~ Pause control frame was transmitted since the pause refresh timer was set.

40. (currently amended) The ~~apparatus~~ system according to claim 38, wherein:

said second MAC client includes means for setting a pause delay timer each time a ~~PAUSE~~ Pause control frame is transmitted,

a ~~PAUSE~~ Pause control frame being transmitted at the expiration of the pause delay timer if congestion conditions have changed since the last ~~PAUSE~~ Pause control frame was transmitted.

41. (currently amended) The ~~apparatus~~ system according to claim 40, wherein:

the pause delay timer is of shorter duration than the pause refresh timer.

42. (currently amended) ~~An apparatus~~ A system for providing flow control for multiple signal streams over a single ~~ETHERNET~~ Ethernet for providing flow control for multiple signal streams over a single ~~ETHERNET~~ Ethernet link, comprising:

means for receiving MAC frames from a MAC client over the ~~ETHERNET~~ Ethernet link, each frame containing a PDU and an indication of the stream to which the PDU belongs;

a plurality of buffers, one buffer associated with each stream;

means for decapsulating the PDUs and storing each PDU in a buffer associated with the stream indicated in the MAC frame;

means for monitoring the fullness of each buffer; and

means for transmitting a ~~PAUSE~~ Pause control frame to the MAC client, the ~~PAUSE~~ Pause control frame indicating the fullness condition of each buffer.

43. (currently amended) The ~~apparatus~~ system according to claim 42, wherein:

the ~~PAUSE~~ Pause control frame includes a single bit identifier for each buffer for indicating the fullness condition of the associated buffer.

44. (currently amended) The ~~apparatus~~ system according to claim 43, wherein:

each single bit identifies an ~~XON/XOFF~~ Xon/Xoff condition.

45. (currently amended) The ~~apparatus~~ system according to claim 42, wherein:

the ~~PAUSE~~ Pause control frame includes a two bit identifier for each buffer for indicating the fullness condition of the associated buffer.

46. (currently amended) The ~~apparatus~~ system according to claim 45, wherein:

each two bit identifier identifies an ~~XON/XOFF/NOCHANGE~~  
Xon/Xoff/NoChange condition.

47. (currently amended) The ~~apparatus~~ system according to claim 42, wherein:

the ~~PAUSE~~ Pause control frame includes a ~~PAUSE~~ Pause timer value.

48. (currently amended) The ~~apparatus~~ system according to claim 47, wherein:

the ~~PAUSE~~ Pause timer value is set to zero when the ~~PAUSE~~ Pause control frame indicates that no buffer is experiencing congestion.

49. (currently amended) The ~~apparatus~~ system according to claim 48, wherein:

the ~~PAUSE~~ Pause timer value is set to a pre-programmed Pause Time Value when the ~~PAUSE~~ Pause control frame indicates that at least one buffer is experiencing congestion.

50. (currently amended) The ~~apparatus~~ system according to claim 49, further comprising:

a pause refresh timer; and

means for resetting the pause refresh timer each time a ~~PAUSE~~ Pause control frame is transmitted, wherein

a ~~PAUSE~~ Pause control frame is transmitted at the expiration of the pause refresh timer if no ~~PAUSE~~ Pause control frame was transmitted since the pause refresh timer was set.

51. (currently amended) The ~~apparatus~~ system according to claim 50, further comprising:

a pause delay timer; and

means for resetting the pause delay timer each time a ~~PAUSE~~ Pause control frame is transmitted, wherein

a ~~PAUSE~~ Pause control frame is transmitted at the expiration of the pause delay timer if congestion conditions have changed since the last ~~PAUSE~~ Pause control frame was transmitted.

52. (currently amended) The ~~apparatus~~ system according to claim 51, wherein:

the pause delay timer is of shorter duration than the pause refresh timer.

53. (currently amended) ~~An apparatus~~ A system for providing flow control for multiple signal streams over a single ~~ETHERNET~~ Ethernet for providing flow control for multiple signal streams over a single ~~ETHERNET~~ Ethernet link, comprising:

a first MAC (media access control) client; and

a second MAC client coupled to said first MAC client by the ~~ETHERNET~~ Ethernet link,

said first MAC client having

at least one buffer coupled to a source of PDUs (protocol data units) from multiple streams,

an addressing and scheduling module coupled to said at least one buffer, said addressing and scheduling module encapsulating each PDU in a MAC frame which includes an identification of the stream to which the PDU belongs,

a MAC transmitter coupled to said addressing and scheduling block and to the ~~ETHERNET~~ Ethernet link, said MAC transmitter transmitting the MAC frames over the ~~ETHERNET~~ Ethernet link to said second MAC client,

said second MAC client having

a MAC receiver coupled to said ~~ETHERNET~~ Ethernet link, said MAC receiver receiving the MAC frames transmitted by said first MAC client,

a receive addressing module coupled to said MAC receiver, said receive addressing module decapsulating each PDU,

a plurality of port buffers coupled to said receive addressing module, each

port buffer being associated with the stream identified in the MAC frame from which each PDU was decapsulated,

a congestion monitor coupled to said port buffers, said congestion monitor monitoring each buffer for fullness, and

a downstream MAC transmitter coupled to said congestion monitor, said downstream MAC transmitter transmitting a ~~PAUSE~~ Pause control frame to said first MAC client, the ~~PAUSE~~ Pause control frame indicating the fullness condition of each buffer.

54. (currently amended) The ~~apparatus~~ system according to claim 53, wherein:

said first MAC client a downstream MAC receiver coupled to the ~~ETHERNET~~ Ethernet link and said addressing and scheduling module, whereby transmission of PDUs belonging to a stream associated with a buffer indicated as congested by the ~~PAUSE~~ Pause control frame is temporarily halted.



55. (currently amended) ~~An apparatus~~ A system for providing flow control for multiple signal streams over a single ~~ETHERNET~~ Ethernet for providing flow control for multiple signal streams from a MAC client over a single ~~ETHERNET~~ Ethernet link, comprising:

- a MAC receiver coupled to the ~~ETHERNET~~ Ethernet link, said MAC receiver receiving MAC frames from the MAC client over the ~~ETHERNET~~ Ethernet link, each frame containing a PDU and an indication of the stream to which the PDU belongs;
- a plurality of buffers, one buffer associated with each stream;
- a receive addressing module coupled to said MAC receiver and to said buffers, said receive addressing module decapsulating the PDUs and storing each PDU in a buffer associated with the stream indicated in the MAC frame;
- a congestion monitor coupled to said buffers, said congestion monitor monitoring the fullness of each buffer; and
- a MAC transmitter coupled to said congestion monitor, said MAC transmitter transmitting a ~~PAUSE~~ Pause control frame to the MAC client, the ~~PAUSE~~ Pause control frame indicating the fullness condition of each buffer.